

B

TRIBHUVAN UNIVERSITY  
INSTITUTE OF ENGINEERING  
**Examination Control Division**  
2081 Chaitra

Exam.	Regular (New Course)		
Level	BE	Full Marks	60
Programme	BEI, BCT	Pass Marks	24
Year / Part	II / I	Time	3 hrs.

**Subject: - Microprocessors (ENEX 201)**

- ✓ Candidates are required to give their answers in their own words as far as practicable.
- ✓ Attempt All questions.
- ✓ The figures in the margin indicate Full Marks.
- ✓ Assume suitable data if necessary.

1. Define stored program concept. Explain briefly about Von Neumann Architecture. [2+4]
2. Define addressing mode. Explain briefly about different types of addressing mode used in 8085 with suitable example. [2+5]
3. Write a program in 8085 to transfer 8-bit number from one table to other by setting bit D<sub>5</sub> if the number is less than 50 H else transfer the number by resetting bit D<sub>6</sub>. [6]
4. Explain 8086 microprocessor with its internal architecture. Explain flags in 8086 Microprocessor. [6+3]
5. Write an Assembly language programming in 8086 to display "Programming is Fun" in screen. [6]
6. Explain in brief about 8255A PPI with its block diagram. [6]
7. Design an address decoding circuit to interface one 2K × 8 ROM chip, one 2K × 8 RAM chip and one 4K × 8 EPROM chip with 8085 microprocessor consecutively starting from the memory location A000H. [6]
8. What is an interrupt? Explain about the interrupt processing in 8085. [2+4]
9. Differentiate between RISC and CISC architecture. [4]
10. Compare and contrast between accumulator based and register based architecture. [4]

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